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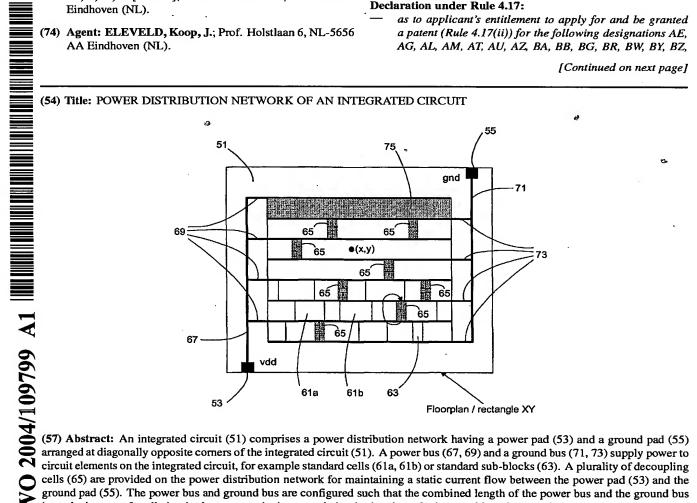
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cells (65) are provided on the power distribution network for maintaining a static current flow between the power pad (53) and the ground pad (55). The power bus and ground bus are configured such that the combined length of the power bus and the ground bus is made the same for all circuit elements on the integrated circuit, thereby reducing on chip voltage variation.



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